

PECIFICATION FOR LCD MODULE

MODULE NO: YB-TG480272C52A-C-A0

Doc.Version:00

C 4		V CI SIOII.UU	
Customer Appr	oval:		
☐ Accept			☐ Reject
YEEBO	NAME	SIGNATURE	DATE
Prepare	Electronic Engineer	袁江和	2019/8/13
Check	Mechanical Engineer	るナ盛	2019/8/13
Verify		PAK To	2019/8/13
Approval		Sunray	2019/8/13
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■ APPROVA	L FOR SPECIFICATIONS	ONLY	
□ APPROVA	L FOR SPECIFICATIONS	AND SAMPLE	
			W/IN ADD005 02
			WIMRD005-02-

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1. Revision History

Sample Version	DOC. Version	DATE		DESCRIPTION	CHANGED BY
A0	00	2019-08-08	Spec only	First issue	W.G.S
			1 ,		



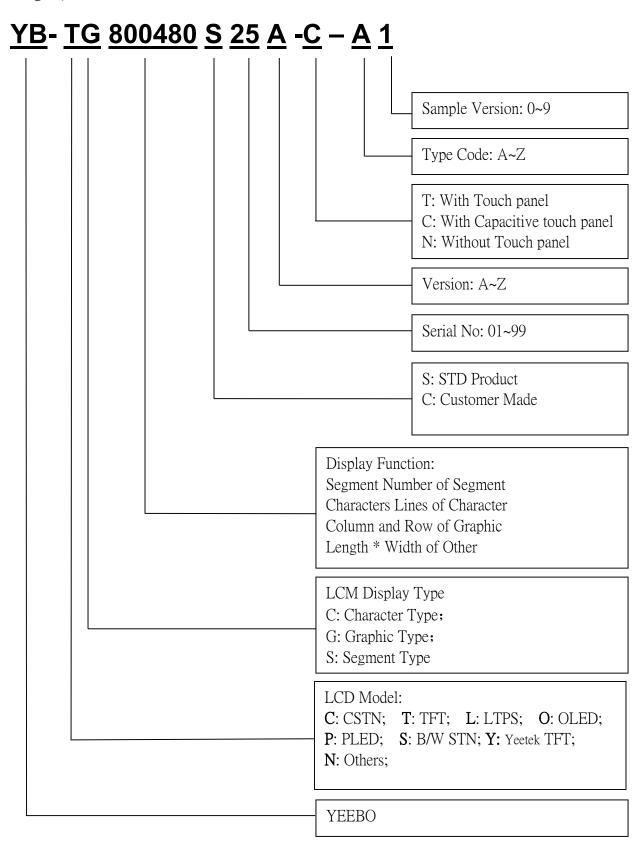
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3. Module Numbering System:

(example)



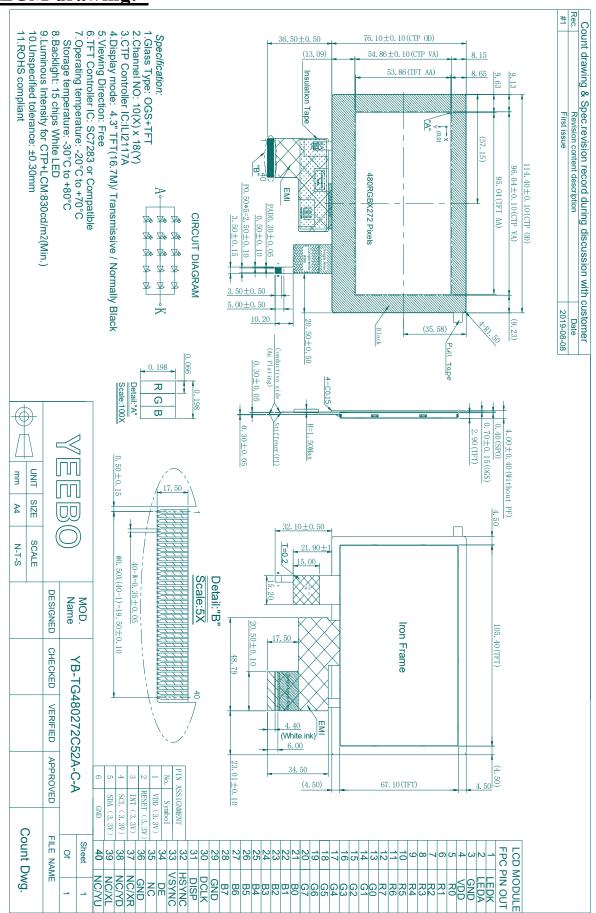


4. General Specification:

ITEM	CONTENTS
Module Size	114.40(W) * 76.10(H) * 4.00(T) mm
Display Size(Diagonal)	4.3 inch
Display Format	480(RGB)* 272 Pixels
Active Area	95.04(W) * 53.856(H) mm
View Area	96.04(W) * 54.856(H) mm
Pixel Pitch	0.198*0.198 mm
LCD Type	TFT(16.7M) / Transmissive/Normally Black
The Viewing Direction:	Free
Controller IC	SC7283
CTP IC	ILI2117A
Weight	TBD
CTP Interface	I ² C
Fireware	TBD
Test Configuration	TBD



5. LCM drawing:





6. Electrical Characteristics

6-1 Absolute Maximum Ratings

6-1-1 Absolute Maximum Ratings (TFT)

(Ta=25°C VSS=0V)

Item	Symbol	Min.	Туре	Max.	Unit	Remark
Power Supply voltage	VDD	-0.5	-	+4.0	Volt	
Operating Temperature	Topr	-20	-	70	$^{\circ}\!\mathbb{C}$	
Storage Temperature	Tstg	-30	-	80	$^{\circ}$ C	

Note1: Absolute maximum rating is the limit value beyond which the IC maybe broken. They do not assure operations.

6-1-2 Absolute Maximum Ratings (TP)

(Ta=25°C VSS=0V)

Item	Symbol	Min	Тур	Max	Unit
System power supply voltage	VDD			3.6	V
High voltage power supply	V _{PVDD_CP}		3.6	3.7	V
Analog input voltage	V _{INANA}			VDD	V
Digital input voltage	V _{INDIG}			5	V
Storage temperature	T _{STG}	-40		150	°C

Notes: Stresses above those listed in Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and does not imply functional operation of the device. Exposure to absolute maximum ratings for extended periods may affect device reliability.

6-2 Operating Conditions

6-2-1 Operating Conditions (TFT)

(Ta=25°C)

	•	,			` `	
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply voltage	VDD	-	3.0	3.3	3.6	Volt
T 1T 437 14	VIH	-	0.7*VDD	1	VDD	Volt
Level Input Voltage	VIL	-	GND	-	0.3*VDD	Volt
Power Supply Current for LCM	IDD	-	-	TBD	-	mA

Note1:GND=0V

6-2-2 Operating Conditions (TP)

 $(Ta=25^{\circ}C)$

Table 5-2: Power Supply

Item	Symbol	Min	Тур.	Max	Unit
System power supply voltage	VDD	2.8	7	3.3	V
Ambient operating temperature	T _A	-40	Y	85	℃
Junction Temperature	TJ			125	$^{\circ}$

Table 5-3: DC Characteristics (Topr = 25℃)

Item	Symbol	Min	Тур.	Max	Unit
Input Voltage, High 1	(V _{IH1}) ¹	1			*V
Input Voltage, High 2	(V _{IH2}) ²	1.3			V
Input Voltage, Low	(V _{IL})			0.5	V
Output Voltage, High 1	(V _{OH1}) ³		See Note3		V
Output Voltage, High 2	(V _{OH2}) ⁴	V _{VDD} - 0.1			V
Output Voltage, Low	(V _{OL})			0.1	V

Specifications are subjected to change without notice.

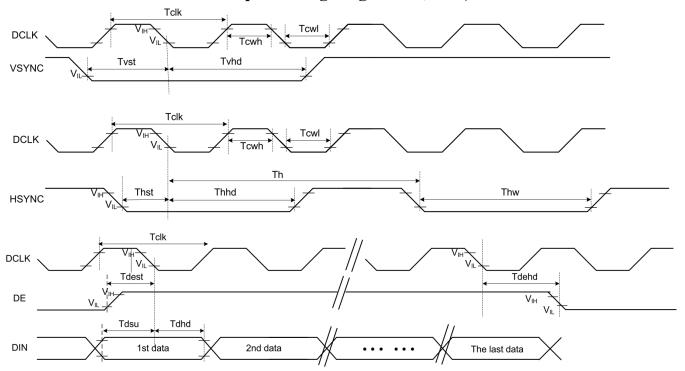
Notes

- 1. V IH1 includes pins CHIP_EN, SDA, SCL, INT
- 2. V_{IH2} includes pin EXT_CLK
- 3. V_{OH1} is for INT output voltage level which is programmable by registers. Typical values are 1.2V/1.5V/1.8V/V_{VDD}.
- V_{OH2} refers to other digital pins.



6-3 Timing Characteristics

6-3-1 Clock and data input timing diagram (TFT)



6-3-2 Clock and data input timing diagram (TP)

5.4 AC Characteristics of the SDA and SCL on I²C interface

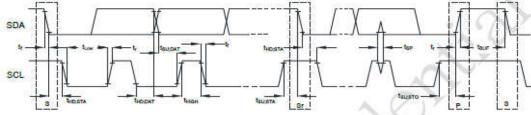


Figure 5-1: The timing of I²C Interface

Table 5-5: Characteristics of the SDA and SCL bus lines

Symbol	Parameter	100KHz			400KHz		
Symbol	Parameter	Min	Max	Unit	Min	Max	Unit
fact	SCL clock frequency	0	100	kHz	0	400	KHz
t _{HD,STA}	Hold time (repeated) START condition. After this period, the first clock pulse is generated	4.0	-	μs	0.6	_	μs
tow	LOW period of the SCL clock	4.7	28	μs	1.3	929	μs
t _{HIGH}	HIGH period of the SCL clock	4.0	257	μs	0.6	220	μѕ
t _{SU;STA}	Set-up time for a repeated START condition	4.7	- CE	μз	0.6	555	μѕ
tho;DAT	Data hold time	0	3.45	μs	0	0.9	μs
tsu;DAT	Data set-up time	250	-	ns	100	-	ns
t,	Rise time of both SDA and SCL signals	-5	1000	ns	-	300	ns
t,	Fall time of both SDA and SCL signals	-	300	ns		300	ns
t _{su;sto}	Set-up time for STOP condition	4.0	25	μs	0.6	223	μs
t _{BUF}	Bus free time between a STOP and START condition	4.7	- 1	μѕ	1.3		μѕ



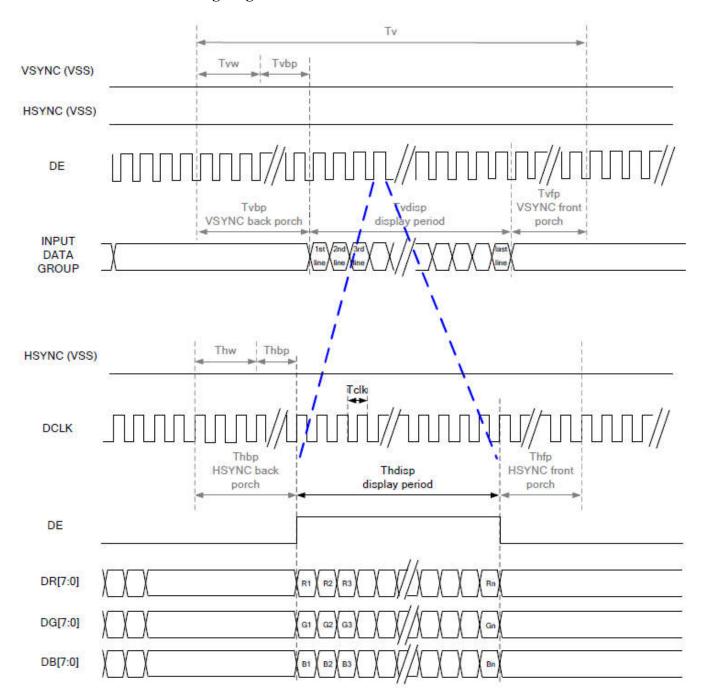
6-4 RGB input timing table

		480RGB)	K 272 Re	solutio	n Timing	g Table	
	Item	Symbol	Min.	Тур.	Max.	Unit	Remark
DCL	(Frequency	Fclk	8	9	12	MHz	
DC	LK Period	Tclk	83	111	125	ns	
	Period Time	Th	485	531	598	DCLK	
	Display Period	Thdisp		480		DCLK	
HSYNC	Back Porch	Thbp	3	43	43	DCLK	By H_BLANKING setting
	Front Porch	Thfp	2	8	75	DCLK	
	Pulse Width	Thw	2	4	43	DCLK	
	Period Time	Tv	276	292	321	HSYNC	
	Display Period	Tvdisp		272		HSYNC	
VSYNC	Back Porch	Tvbp	2	12	12	HSYNC	By V_BLANKING setting
	Front Porch	Tvfp	2	8	37	HSYNC	
	Pulse Width	Tvw	2	4	12	HSYNC	

Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.



6-5 DE mode timing diagram



RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

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7. Optical Characteristics:

T4	Itam		C 1:4:	Specifications			TI24	Nete
Item		Symbol	Conditions	Min	Тур	Max	Unit	Note
Transmit	ttance	T(%)	-	-	6.6	-	-	-
Contrast Ratio		CR	⊕=0 Normal Viewing angle	640	800	-	-	(1) (2)
Response	e time	TR+TF	-	-	30	40	ms	(1) (3)
	Hor.	Өх+		-	80	-	dog	-
Viewin	1101.	Өх-	CR≧10	-	80	-		
g angle	Ver.	Өу+	$O_{\rm N} = 10$	-	80	-	deg.	
	V CI.	Өу-		-	80	-		

Measuring Condition

1. Measuring surrounding: dark room

2. Ambient temperature: 25±2°C

3. 30 min. Warm-up time.

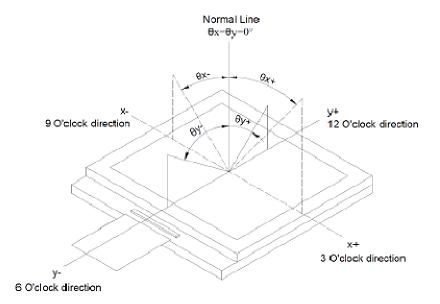
Color of CIE Coordinate:

Item	Item		Condition	Min.	Тур.	Max.
	Red	х		0.563	0.613	0.663
		у	θ = 0° Backlight Color Degree	0.302	0.352	0.402
01 11 11	Green	х		0.337	0.387	0.437
Chromaticity		у		0.510	0.560	0.610
Coordinates (Transmissive)	Blue	х		0.095	0.145	0.195
(Transmissive)		у		0.073	0.123	0.173
		х		0.290	0.340	0.390
	White	у		0.310	0.360	0.410

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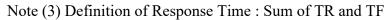
Note (1) Definition of Viewing Angle:

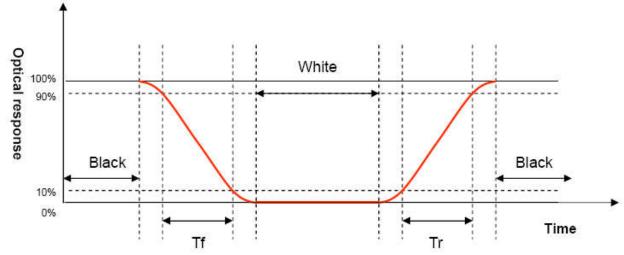


Note (2) Definition of Contrast Ratio(CR): measured at the center point of panel

Contrast ratio (CR)= Photo detector output when LCD is at "White" state

Photo detector output when LCD is at "Black







8. Interface Pin Assignment: 8-1 LCM FPC interface

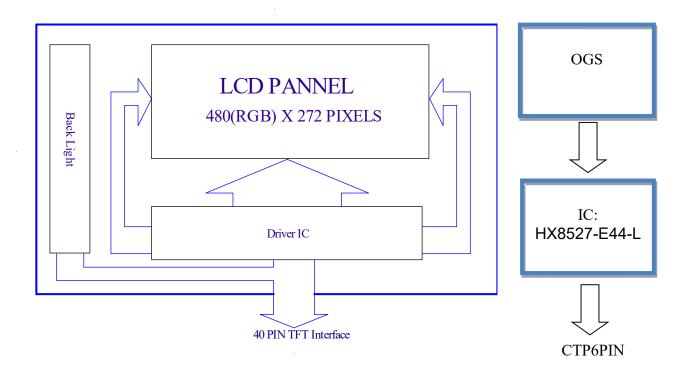
No.	Symbol	Function
1	LEDK	Cathode of LED backlight
2	LEDA	Anode of LED backlight
3	GND	GND Power ground
4	VDD	Power voltage.
5~12	R0∼ R7	Digital data input.R0(LSB),R7(MSB)
13~20	G0~ G7	Digital data input.G0(LSB),G7(MSB)
21~28	B0∼ B7	Digital data input.B0(LSB),B7(MSB)
29	GND	Power ground
30	DCLK	Data clock signal input
31	DISP	Display on/off mode control. (a) DISP=L, standby mode. (b) DISP=H, normal display mode.
32	HSYNC	Horizontal sync signal input
33	VSYNC	Vertical sync signal input
34	DE	Data enable input.
35	NC	No connection
36	GND	Power ground
37	NC/XR	No connection, reserve for TP interface.
38	NC/YD	No connection, reserve for TP interface.
39	NC/XL	No connection, reserve for TP interface.
40	NC/YU	No connection, reserve for TP interface.

8-2: CTP Pin Description:

No.	Symbol	Function
1	VDD3.3V	Power supply
2	RESET	System reset signal input, active low
3	INT	Indicate coordinate data ready
4	SCL	I2C Serial Clock
5	SDA	I2C Serial Data
6	GND	Ground



9. Block Diagram:





10. Backlight:

- 1. Standard Lamp Styles (Edge Lighting Type):
 The LED chips are distributed over the edge light area of the illumination unit, which gives the less power consumption:
- 2. The Main Advantages of the LED Backlight are as following:
 - 2.1 The brightness of the backlight can simply be adjusted. By a resistor or a potentiometer.

3. Data About LED Backlight:

 $(Ta=25^\circ)$

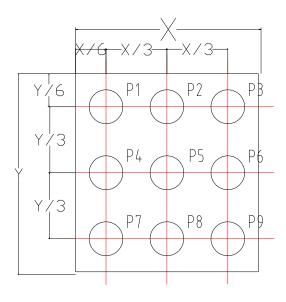
PARAMETER	Sym.	Min.	Тур.	Max.	Unit	Test Condition	Note
Supply Current	I	-	60	-	mA	-	
Supply Voltage	V	14.5	15.0	15.5	V		
Luminous Intensity for CTP+ LCM	Iv	830		-	Cd/m ²	If=60mA	2
Uniformity for LCM+CTP	ı	70	-	-	%	11 0011111	3
Life Time	-	50000	-	-	Hr.		4
Color	White						

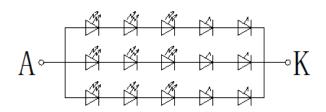
NOTE:

- 1. Backlight Only
- 2. Average Luminous Intensity of P1-P9
- 3. Uniformity = Min/Max * 100%
- 4. LED life time defined as follows: The final brightness is at 50% of original brightness

Measured Method: (X*Y: Light Area)

Internal Circuit Diagram





(Effective spatial Distribution)

Using aperture of 1°, distance 50cm.



11. Standard Specification for Reliability: 11–1. Standard Specifications for Reliability of (LCD+CTP) Module

No	Item	Description
01	High temperature operation	The sample should be allowed to stand at 70°C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
02	Low temperature operation	The sample should be allowed to stand at -20°C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
03	High temperature storage	The sample should be allowed to stand at 80°C for 240 hours under no-load condition, and then returning it to normal temperature condition, and allowing it stand for 2 hours.
04	Low temperature storage	The sample should be allowed to stand at -30°C for 240 hours under no-load condition, then returning it to normal temperature condition, and allowing it stand for 2 hours.
05	Moisture storage	The sample should be allowed to stand at 60°C,90%RH MAX for 240 hours under no-load condition, then taking it out and drying it at normal temperature for 2 hours.
06	Thermal shock storage	The sample should be allowed to stand the following 10 cycles: -30° C for 30 minutes \rightarrow normal temperature for 5 minutes \rightarrow +80°C for 30 minutes \rightarrow normal temperature for 5 minutes, as one cycle.
07	Packing vibration	Frequency range: 10Hz ~ 55Hz Amplitude of vibration: 1.5mm X,Y,Z 2 hours for each direction. Sweep time: 12 min
08	Packing drop test	According to ISTA 1A 2001.
09	Electrical Static	Air: ± 6 KV 150 pF/ 330Ω 5 times
	Discharge	Contact: $\pm 4KV \ 150pF/330\Omega \ 5$ time

^{*}Sample size for each test item is 3~5pcs

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11 - 2. Testing Conditions and Inspection Criteria

For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in Table 12.2, Standard specifications for Reliability have been executed in order to ensure stability.

No	Item	Test Model	In section Criteria
01	Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
02	Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
03	Appearance	Visual inspection	Defect free.

11-3. MTBF

MTBF	Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature (25 \pm 5 $^{\circ}$ C), normal humidity (50 \pm 10% RH), and in area not exposed to direct sun light.
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12. Specification of Quality Assurance:

12-1. Purpose

This standard for Quality Assurance should affirm the quality of LCD module products to supply to purchaser by YEEBO CORPORATION (Supplier).

12-2. Standard for Quality Test

a. Inspection:

Before delivering, the supplier should take the following tests, and affirm the quality of product.

b. Electro-Optical Characteristics:

According to the individual specification to test the product.

c. Test of Appearance Characteristics:

According to the individual specification to test the product.

d. Test of Reliability Characteristics:

According to the definition of reliability on the specification for testing products.

e. Delivery Test:

Before delivering, the supplier should take the delivery test.

- (i) Test method: According to ISO2859-1. General Inspection Level

 ☐ take a single time.
- (ii) The defects classify of AQL as following:

Major defect: AQL = 0.40Minor defect: AQL = 1.0Total defects: AQL = 1.0

12-3. Non- conforming Analysis & Deal With Manners

- a. Non-conforming Analysis:
 - (i) Purchaser should supply the detail data of non- conforming sample and the non-conforming.
 - (ii) After accepting the detail data from purchaser, the analysis of non- conforming should be finished in two weeks.
 - (iii) If supplier can not finish analysis on time, must announce purchaser before 3 days.
- b. Disposition of non- conforming:
 - (i) If find any product defect of supplier during assembly time, supplier must change the good product for every defect after recognition.
 - (ii) Both supplier and customer should analyze the reason and discuss the disposition of non-conforming when the reason of nonconforming is not sure.

12-4. Agreement items

Both sides should discuss together when the following problems happen.

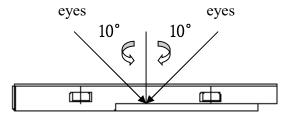
- a. There is any problem of standard of quality assurance, and both sides should think that must be modified.
- b. There is any argument item which does not record in the standard of quality assurance.
- c. Any other special problem.

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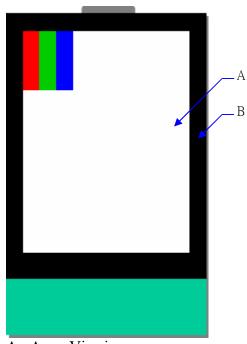


12-5. Standard of The Product Appearance Test

- a. Manner of appearance test:
- (i) The test must be under 20W × 2 or 40W fluorescent light, and the distance of view must be at 30±5cm.
 - (ii) When test the model of transmissive product must add the reflective plate.
 - (iii)The test direction is base on around 10° of vertical line.
 - (iiii)Temperature: 25±5°C Humidity: 60±10%RH



(iv) Definition of area:



- A. Area: Viewing area.
- B. Area: Out of viewing area.

(Outside viewing area)

- b. Basic principle:
- (i) It will accord to the AQL when the standard can not be described.
- (ii) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.
- (iii) Must add new item on time when it is necessary.
 - d. Standard of inspection: (Unit: mm)



12-6. Inspection specification

Item		pecification	Unit : mm	AQL
Electrical Testing	1.5 Missing char1.6 Display malf1.7 No function of	acter, dot or icon. unction. or no display. umption exceeds product g angle defect.	segment contrast defect. t specifications.	0.65
explosion-proof film bubble/Concave and convex point/indentation / Contamination	allowed. 2. Printing ink 3. The particle	peel off is not allowed.	D=(x+y)/2 1. Product's front side checked side ignored, but light leakage is not is removable by cleaning ts within 10mm	



	D	Acceptable numbers	7	
		ignored (No more	y	
	≤ 0. 15	than five spots within	X	
		5mm)	· A ·	
	0. 15 <d≤0. 3<="" td=""><td>3</td><td>5 () (0</td><td></td></d≤0.>	3	5 () (0	
	0.3 <d≤0.5< td=""><td>2</td><td>D=(x+y)/2</td><td></td></d≤0.5<>	2	D=(x+y)/2	
	D>0. 5	NG	1.Product's front side checked	
	allowed. 2.Printing ink per	s specification, back side of the side of	de ignored, but light leakage is not	
Black spots / White spots /Bright spots/ Color spots /polluted inside/ punctured	* Densely spaced	d: No more than two spot	ts within 10mm	2.5



	EDO GROUI	T	T	1		
	W	L	Acceptable numbers			
		≤ 5	ignored (No more			
	≪0.05		than five lines			
			within 5mm)			
	0. 05 <w≤0. 15<="" td=""><td>≤5</td><td>2</td><td></td><td></td><td></td></w≤0.>	≤ 5	2			
	₩〉0.15		NG			
Fiber, scurf, scratches and other linear defects (not affecting function)	scratches from the	e front sid	not affect to the elect de is acceptable re than two lines within		cannot find the	2.5
Glass edge chipping, edge breakage	Edge breakage can't affect visual effection (edge breakage can't cause damage to circuit); over lens have no visual damage conditions Acceptable e numbers X < 1.5mm, Y < 2mm, Z < T 4					2.5
	Visual broken	is NG,	and there is no po	tential fau	ılt.	
Glass broken		7				0.65
1. V/A printed edges sawtooth inspected	Some contentious defect judged according to samples					
according to this standard	Product	Conditi	ons		1	
2. LOGO's sawtooth	Same size	ignored	th below 0.2 inch (inc , above 0.2 NG gth not accounted	cluded)		2.5
Specific dimension	In accordance w or engineering sa	-	ect outline drawing or	specification ((key dimension)	2.5



Glue overf	low/Frame	1. Glue overflow exceed 0.2mm to the black frame is not allowed.	2.5
	Bonding bubble/ Misalignm ent	FPC golden finger hot pressure's bubble or impurity diameter shall be below 1/2 of the pressed area, pressed deviation shall not exceed 1/2 of the silver line width, and 40X microscope cannot have obvious cracks.	0.65
FPC	Folded mark (minor fault)	Linearity irreversibility folded mark and acute angle folded mark is NG.	2.5
	EMI FILM (minor fault)	Surface broken, scratched ≤ 0.3mm Surface broken below 5mm can be modified by print ink, after modified, the result shall be achieved to EMI	2.5



13. Handling Precaution:

13-1 Handling of LCM

- Don't give external shock.
- Don't apply excessive force on the surface.
- Liquid in LCD is hazardous substance. Must not lick and swallow. when the liquid is attach to your hand, skin, cloth etc. Wash it out thoroughly and immediately.
- Don't operate it above the absolute maximum rating.
- Don't disassemble the LCM.
- The operators should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.
- The modules should be kept in antistatic bags or other containers resistant to static for storage.
- The module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

13-2 Storage

- Store in an ambient temperature of 25±10°C, and in a relative humidity of 50±10%RH. Don't expose to sunlight or fluorescent light.
- Storage in a clean environment, free from dust, active gas, and solvent.
- Store in anti-static electricity container.
- Store without any physical load.

13-3 Soldering

- Use only soldering irons with proper grounding and no leakage.
- Iron: No higher than 280±10°C and less than 3 sec during Hand soldering.
- Rewiring: no more than 2 times.

14. Guarantee:

Our products meet requirements of the environment.

YEEBO ROHS requirement is based on European Union Directive 2011/65/EU(ROHS) Requirements and Update.

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